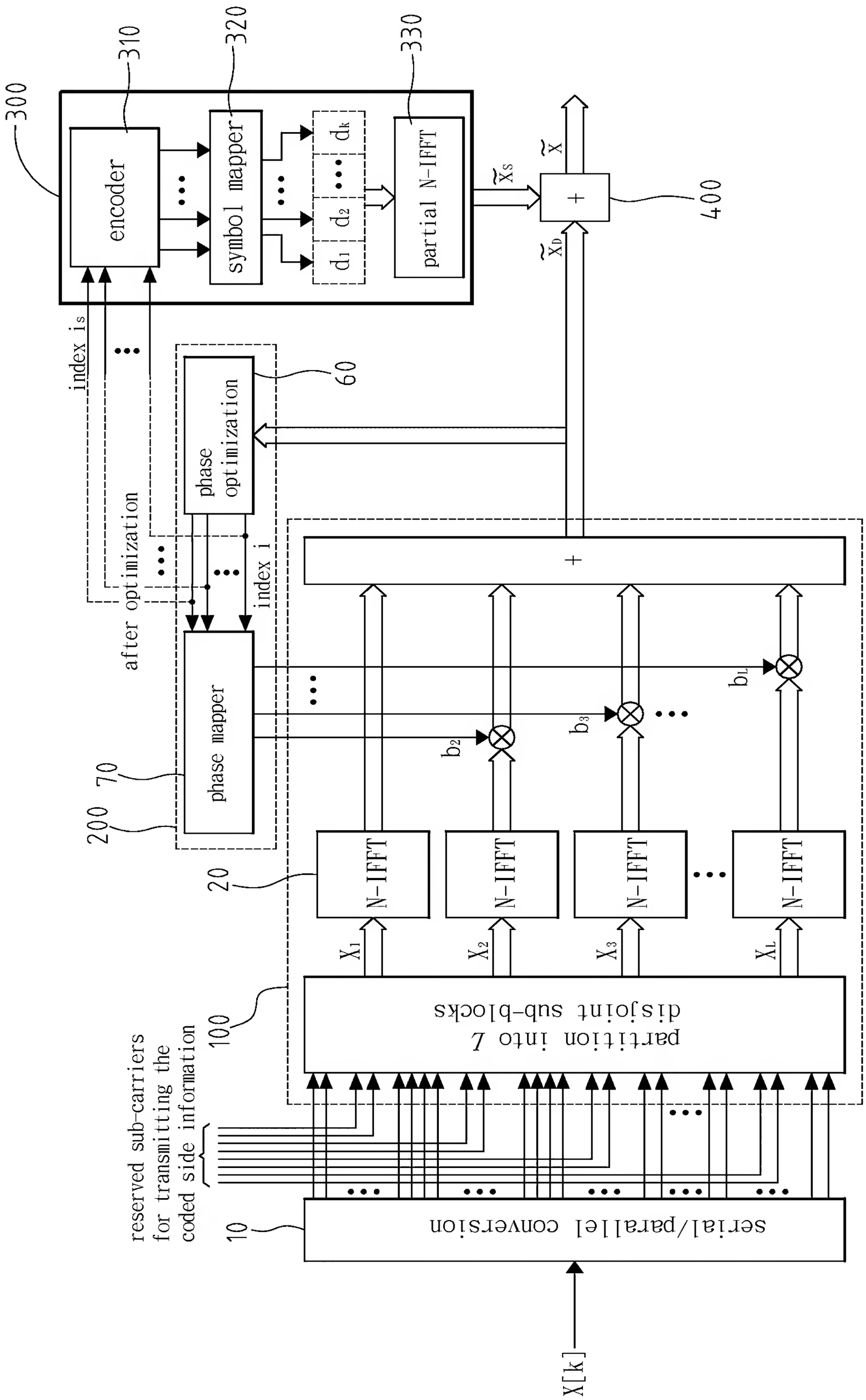
300
310
320
330
encoder
symbol mapper
$d_1$ $d_2$ $d_k$
partial N-IFFT
$\tilde{X}_S$
$\tilde{X}$
$\tilde{X}_D$
+
400
index i
phase optimization
60
after optimization
index i
phase mapper
70
200
$b_2$ $b_3$ $b_L$
20
N-IFFT
$X_1$ $X_2$ $X_3$ $X_L$
partition into $L$ disjoint sub-blocks
100
reserved sub-carriers for transmitting the coded side information
serial/parallel conversion
10
X[k]

FIG. 5